

Please enter the amendment to  
specification page 1.

Re 10/14/2004

Thank you

Appl. No. 10/789,375  
Amdt. dated April 13, 2004  
Preliminary Amendment

PATENT

**Amendments to the Specification:**

Please amend the paragraph under "CROSS REFERENCES TO RELATED APPLICATIONS" to read as follows:

--This application is a continuation of U.S. Application Serial No. 09/969,289, filed October 1, 2001, <sup>now U.S. Patent No. 6,724,763 B2</sup> which is a continuation of Application Serial No. 09/444,550, filed November 22, 1999, <sup>now U.S. Patent No. 6,300,736 B1</sup>, which is related to U.S. Provisional Patent Application Serial No. 60/128,675 (~~Atty. Docket No. 016998-001300~~), filed April 9, 1999, entitled "LOW PIN COUNT DC-MOTOR INTEGRATED DRIVE CIRCUIT," the disclosure of each of which is hereby incorporated by reference in its entirety.--

Please amend the paragraph beginning at page 6, line 20 to read as follows:

--While the invention has been described by way of example there are other features that are well known to those skilled in the art that can also be included in the circuitry as additional features without changing the invention. By way of illustration and example, additional functionality can be implemented in control circuit 103 to vary the ratio of on and off states of the power transistors such as to control the speed of the fan. Such control can be responsive to one or more of several possible signals. In one embodiment, one such ~~[signals]~~ signal includes an additional signal communicated to control circuit 103 from an additional connection to the chip. In another embodiment, one such signal includes a signal from an ~~[on or off chip]~~ on- or off-chip temperature sensor such as to arrange an increase in fan speed when the temperature increases (or a decrease in fan speed, or shut off, when the T decreases). In yet another embodiment, one such signal includes a signal from a comparator circuit which compares the actual motor speed derived from the switching signals or the hall effect device signals with an on- or off-chip preprogrammed reference signal or an externally applied reference signal.--